

**IN THE CLAIMS:**

Claim 1 (Cancelled).

Claim 2 (Currently Amended):      ~~The liquid crystal display device of claim 1,~~ A  
liquid crystal display device, comprising:  
a color filter substrate having a black matrix, and color filter layers at a designated region  
determined by the black matrix;  
an array substrate having a gate bus line and a data bus line crossing perpendicularly and  
defining a unit pixel region,  
a thin film transistor arranged at an intersection of the gate bus line and the data bus line,  
a pixel electrode contacting a drain electrode of the thin film transistor and overlapping  
portions of the gate bus line, the data bus line, an adjacent gate bus line, and an adjacent data bus  
line, wherein the pixel electrode and the data bus line have an overlapping width in a range of  
about 3 to 5 micrometers ( $\mu\text{m}$ ), and the pixel electrode and the adjacent data bus line have an  
overlapping width in a range of about 0 to 2 micrometers ( $\mu\text{m}$ );  
an organic insulating layer on the pixel electrode and the thin film transistor, wherein a  
surface of the array substrate is rubbed in a 315 degree direction; and  
a liquid crystal layer between the array substrate and the color filter substrate.

Claim 3 (Currently Amended): ~~The liquid crystal display device of claim 1, A~~

liquid crystal display device, comprising:

a color filter substrate having a black matrix, and color filter layers at a designated region determined by the black matrix;

an array substrate having a gate bus line and a data bus line crossing perpendicularly and defining a unit pixel region,

a thin film transistor arranged at an intersection of the gate bus line and the data bus line,

a pixel electrode contacting a drain electrode of the thin film transistor and overlapping portions of the gate bus line, the data bus line, an adjacent gate bus line, and an adjacent data bus line, wherein the pixel electrode and the gate bus line have an overlapping width in a range of about 2 to 4 micrometers ( $\mu\text{m}$ ), and the pixel electrode and the adjacent gate bus line have an overlapping width in a range of about 4 to 6 micrometers ( $\mu\text{m}$ );

an organic insulating layer on the pixel electrode and the thin film transistor, wherein a surface of the array substrate is rubbed in a 315 degree direction; and

a liquid crystal layer between the array substrate and the color filter substrate.

Claim 4 (Currently Amended): The liquid crystal display device of claim [[1]] 2,  
wherein the black matrix corresponding to a region of the [[data]] gate bus line is formed having  
[[has]] a width equal to or less than half of a width of the gate bus line.

Claim 5 (Currently Amended): The liquid crystal display device of claim [[1]] 2,  
wherein the black matrix corresponding to a region of the data bus line is formed having [[has]] a  
width equal to or less than half of a width of the data bus line.

Claim 6 (Currently Amended): The liquid crystal display device of claim [[1]] 2,  
wherein the organic insulating layer has a thickness of about 2.5 to 3 micrometers ( $\mu\text{m}$ ).

Claim 7 (Currently Amended): The liquid crystal display device of claim [[1]] 2,  
wherein the organic insulating layer has a reflective index in a range of about 1.5 to 1.6.

Claim 8 (Currently Amended): The liquid crystal display device of claim [[1]] 2,  
wherein the organic insulating layer has a dielectric constant in a range of about 3.3 to 3.5.

Claim 9 (Cancelled).

Claim 10 (Cancelled).

Claim 11 (Cancelled).

Claim 12 (Cancelled).

Claim 13 (Cancelled).

Claim 14 (Cancelled).

Claim 15 (Cancelled).

Claim 16 (Cancelled).

Claim 17 (New): The liquid crystal display device of claim 3, wherein the black matrix corresponding to a region of the gate bus line is formed having a width equal to or less than half of a width of the gate bus line.

Claim 18 (New): The liquid crystal display device of claim 3, wherein the black matrix corresponding to a region of the data bus line is formed having a width equal to or less than half of a width of the data bus line.

Claim 19 (New): The liquid crystal display device of claim 3, wherein the organic insulating layer has a thickness of about 2.5 to 3 micrometers ( $\mu\text{m}$ ).

Claim 20 (New): The liquid crystal display device of claim 3, wherein the organic insulating layer has a reflective index in a range of about 1.5 to 1.6.

Claim 21 (New): The liquid crystal display device of claim 3, wherein the organic insulating layer has a dielectric constant in a range of about 3.3 to 3.5.